



## Ji-Yeon LEE

Patent Attorney

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Ji-Yeon Lee is a patent attorney at Lee International IP & Law. After being admitted as a patent attorney in 2020, Lee has been in charge of patent filing and OA responses in technology fields such as communication, electronics, semiconductors, and IT at the Firm. Before joining the firm, she designed the SoC at the S.LSI division of Samsung Electronics' DS part, and was in charge of designing the AUDIO, CORE, CPU, and GPU that constitute SoC. In addition, she was responsible for the PAD and PACKAGE for full chips.

Ms. Lee graduated Pusan National University in 2011 (B.S., Department of Electrical and Electronic Engineering).

### Practice Area

- Electric/electronic
- Semiconductors
- Patent filing and examination in electronics, semiconductor, communication

### Experience

- Lee International IP & Law (2021-Present)
- Senior Researcher, SOC Development Room of S.LSI Division, Samsung Electronics (2011-2017)

### Education

- Pusan National University (B.S., Department of Electrical and Electronic Engineering, 2011)

## Qualifications

- Admitted as a Korean Patent & Trademark Attorney (2020)

## Etc

- Languages : Korean, English

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